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## \*BIBDATASHEET\*

CONFIRMATION NO. 4703

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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 09/873,334 06/05/2001 PAT 6,596,571 ✓

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2000-170096 06/07/2000 ✓

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 10/22/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> Met after Allowance Examiner's Signature <i>M. C. L.</i> Initials <i>M. C. L.</i>	STATE OR COUNTRY JAPAN	SHEETS DRAWING 25	TOTAL CLAIMS 33	INDEPENDENT CLAIMS 4
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## TITLE

Method of manufacturing semiconductor device

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of )
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